

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: § Patent No.: 7,135,723
Szczypinski et al. § Issued: November 14, 2006
§
Serial No.: 10/810,489 § Group Art Unit: 2814
§
Filed: March 26, 2004 § Examiner: Long Pham
§
For: INTEGRATED CIRCUIT WITH A TEST CIRCUIT

Certificate of Correction Branch
Commissioner for Patents
P.O. Box 1450
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<u>September 26, 2007</u> Date	<u>/Joseph M. Jong/</u> Joseph M. Jong

REQUEST FOR CERTIFICATE OF CORRECTION

Attached is a Certificate of Correction for correcting errors in the Claims section of the printed patent. Applicants submit that the errors mentioned above were not by the applicants, but were made during the printing of the patent.

Respectfully submitted, and
S-signed pursuant to 37 CFR 1.4,

/Joseph M. Jong, Reg. No. 42,698/

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UNITED STATES PATENT AND TRADEMARK OFFICE
CERTIFICATE OF CORRECTION

PATENT NO : 7,135,723

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APPLICATION NO. : 10/810,489

DATED : November 14, 2006

INVENTOR(S) : Szczpinski, et al.

It is certified that errors appear in the above-identified patent and that said Letters Patent are hereby corrected as shown below:

In Claim 10, at Column 7, Line 50, please delete "inteprated" and insert --integrated--.

In Claim 10, at Column 8, Line 6, after "phase", please insert --angle--.

MAILING ADDRESS OF SENDER (Please do not use customer number below):

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